

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING SENT VIA FACSIMILE TO: COMMISSIONER OF PATENTS  
AND TRADEMARKS, P.O. Box 1450, Alexandria, VA 22313, ON March 28, 2005 Sent by: Robert Faber

Robert Faber 3-28-05

Signature & Date

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	
In re application of: Bruce B. Doris, et al.	Date: March 28, 2005
Serial Number: 10/604,190	Examiner: Mai Huong C. Tran
Filed: 6/30/03	Group Art Unit: 2818
Title: HIGH PERFORMANCE CMOS DEVICE STRUCTURES AND METHOD OF MANUFACTURE	IBM Corporation D/18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533-6531

**AMENDMENT**

Commissioner of Patents and Trademarks  
Alexandria, VA 22313

Dear Sir:

Prior to Examination, kindly consider the following remarks.

10/604,190

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